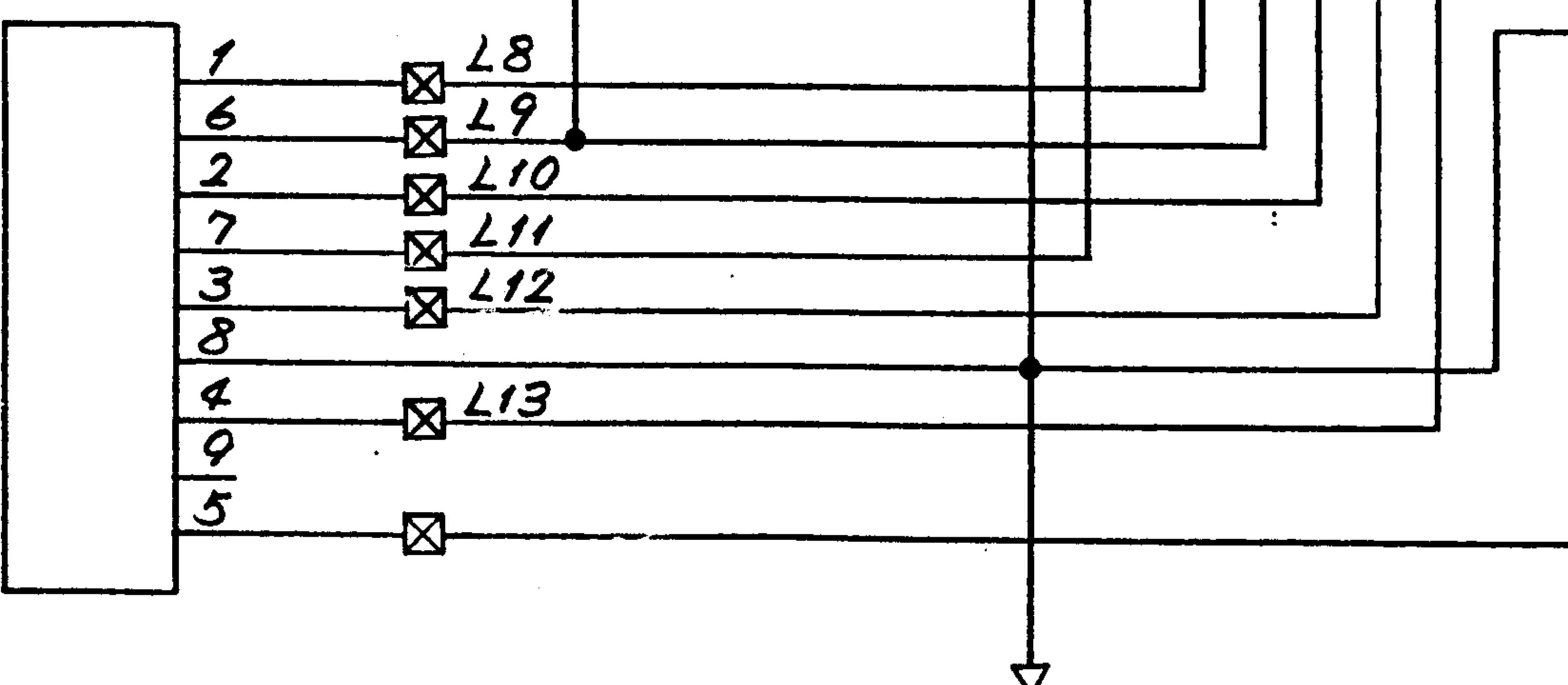
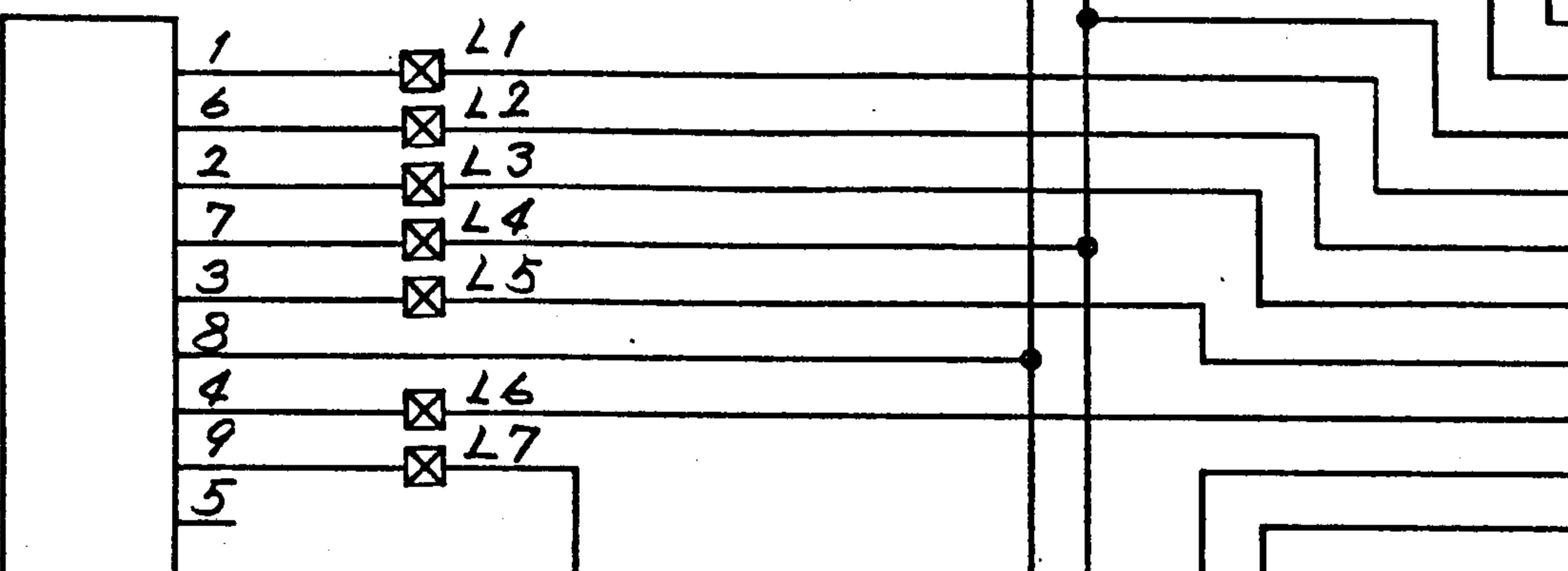
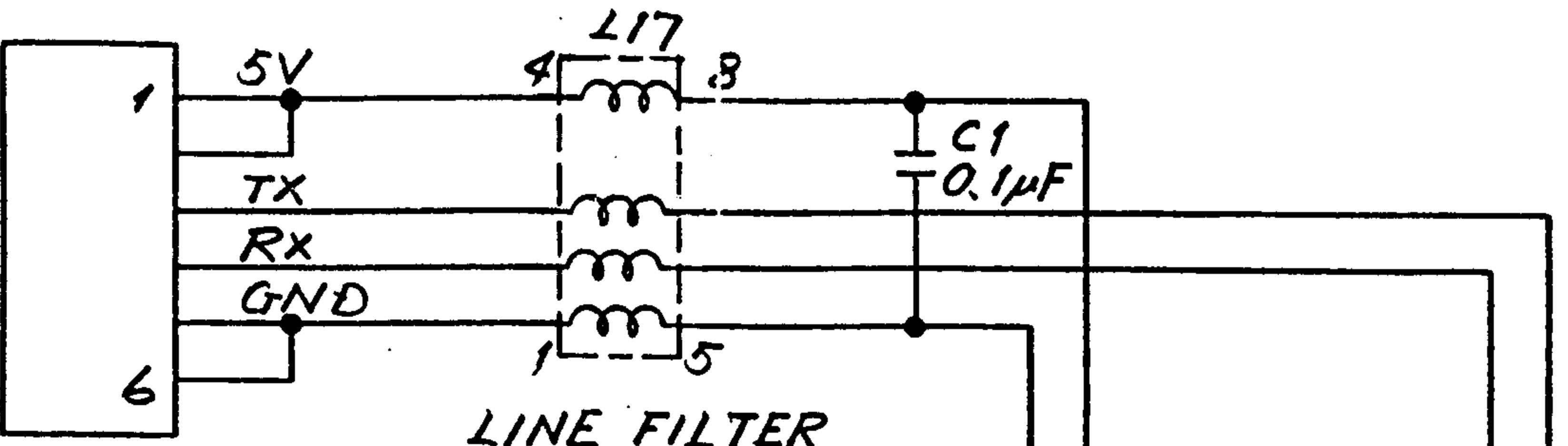


REV	REVISIONS DESCRIPTION	DATE	APPROVED
1	PRELIMINARY RELEASE	3/9/87	<i>J. H. Lee</i>
2	DELETE RES. & CAP. (R1, C2)	4-9-87	
3	REVISED	2/19/87	<i>J. H. Lee</i>



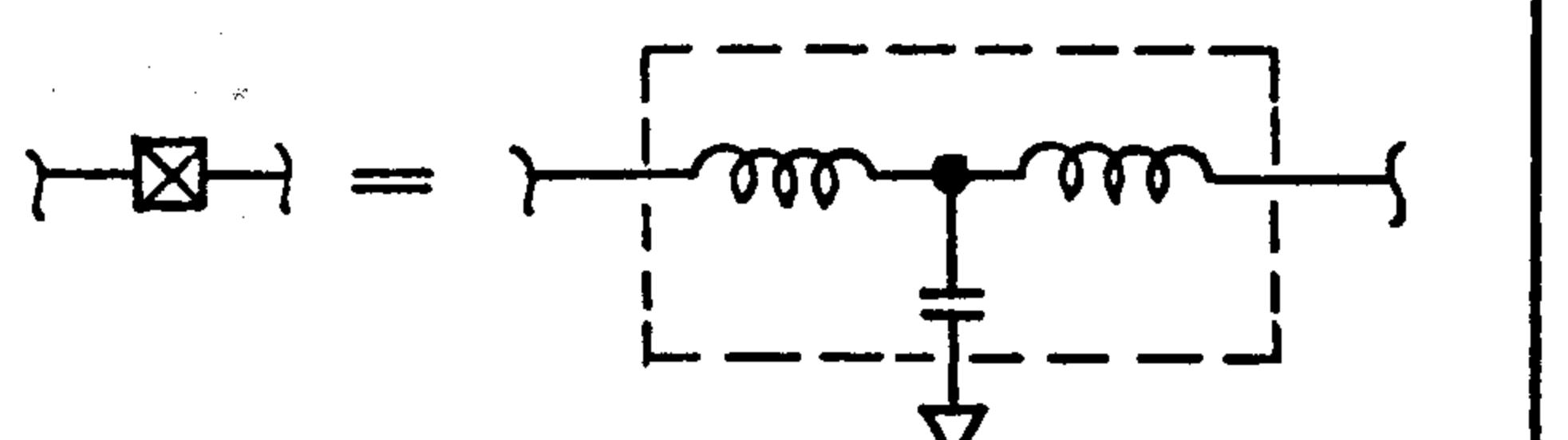
J4
KEYBOARD

18	NC
17	JOY 1-5
16	NC
15	SD FROM CPU
14	SD TO CPU
13	VCC +5V
12	JOY 0-1
11	JOY 0-6
10	JOY 0-2
9	JOY 0-3
8	JOY 0-4
7	JOY 1-1
6	JOY 1-6
5	JOY 1-2
4	JOY 1-3
3	JOY 1-4
2	KEY
1	GND

REFERENCE ONLY

TX DATA
RX DATA
+5V
XB/UP
L BUTTON/FIRE BUTTON
XA/DOWN
YA/LEFT
YB/RIGHT
UP
R BUTTON/FIRE BUTTON
DOWN
LEFT
RIGHT
KEY
GND

DETAIL 1



J1
MAIN

J2
JOY 0


J3
JOY 1


2. USE SUPPLIED P.C.B. (C100063, REV. 1)

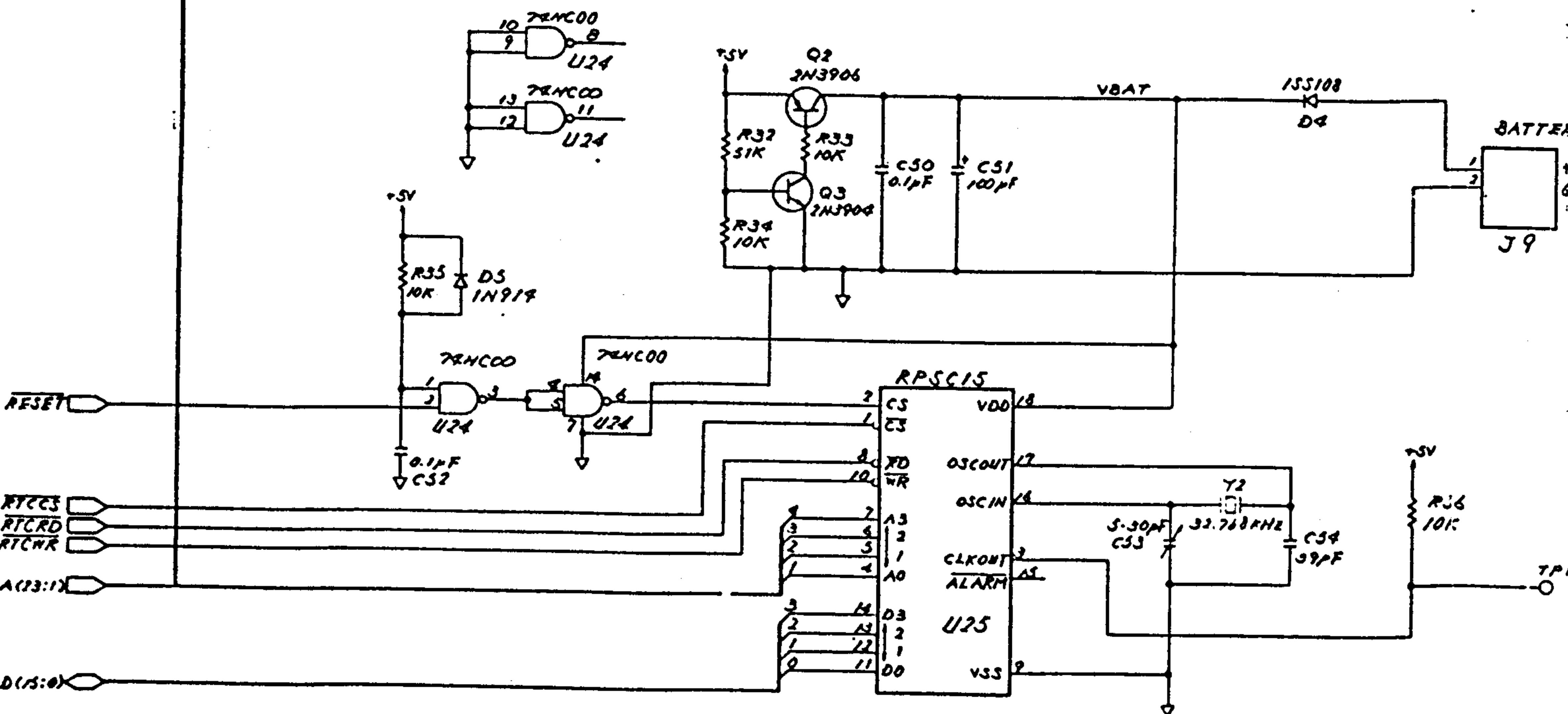
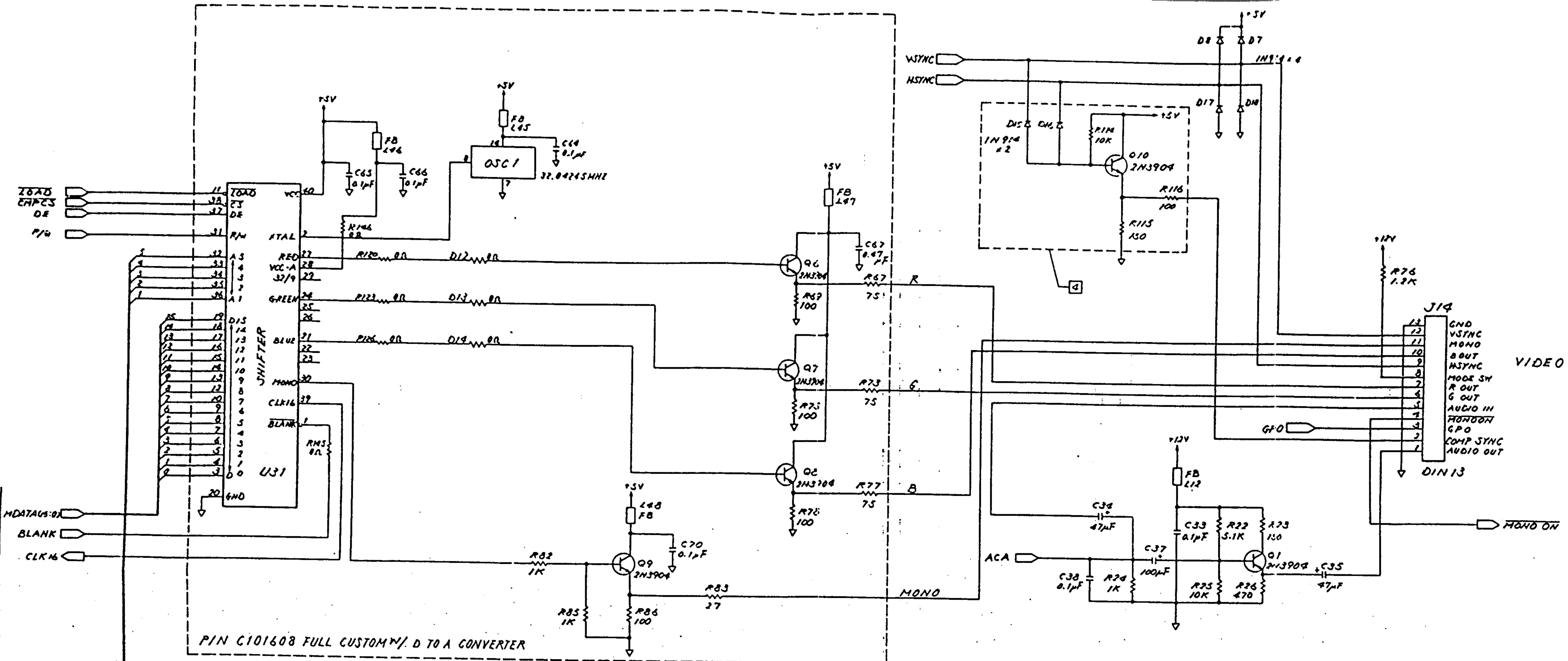
1. NOISE FILTER : SEE DETAIL 1 .

NOTES—UNLESS OTHERWISE SPECIFIED:

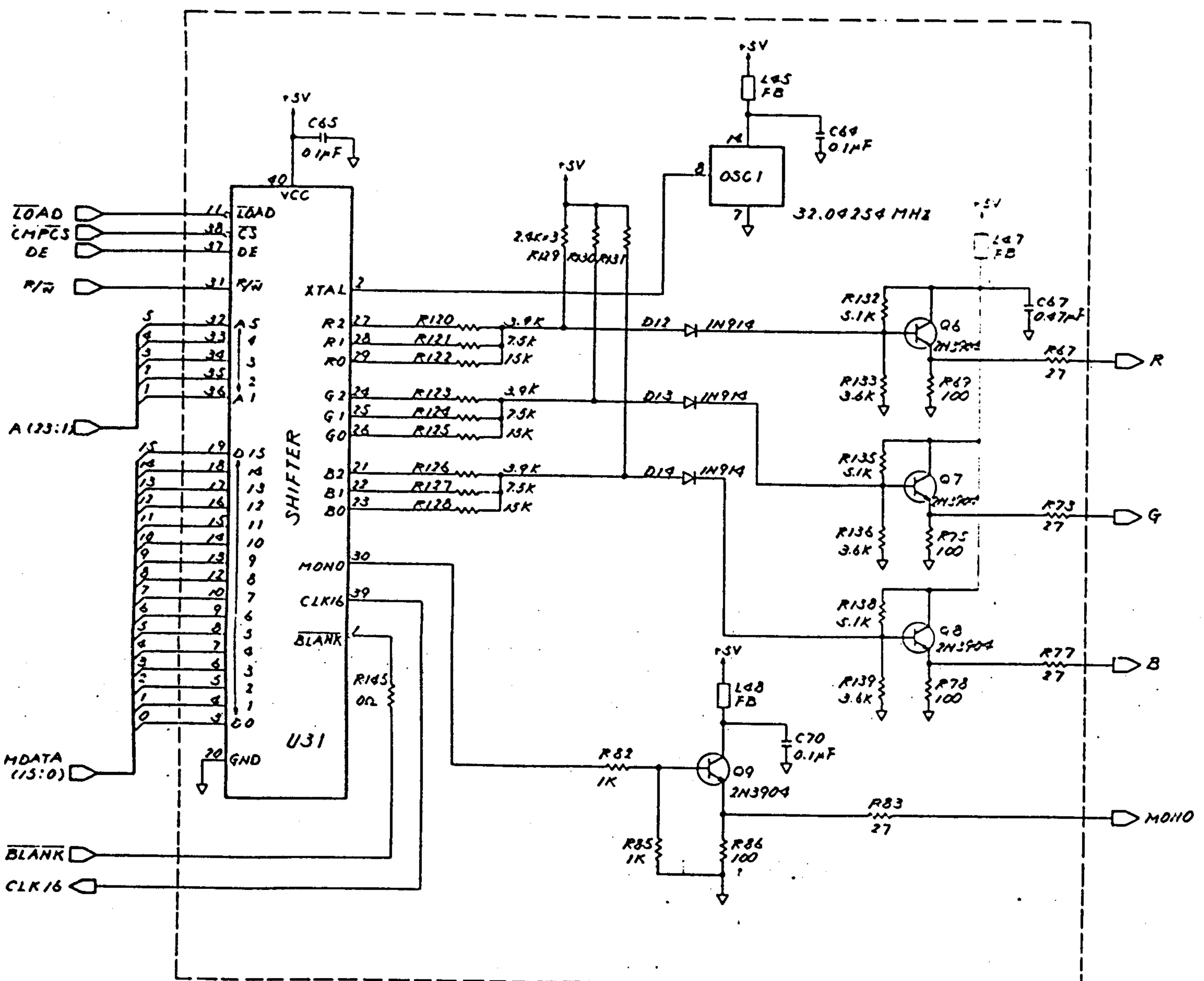
APPLICATION	FINISH	APPROVED	DATE	SCALE	REV
CA200092-XXX	MEGA ST KEYBOARD	I. Yasuda 10-24-86 CHECKED	DATE	ATARI	ATARI (JAPAN) CORPORATION
NEXT ASSY	USED ON	K. Kitagawa 10-27-86 ENGINEER	DATE		Toranomon Kiyoshi Building 3F
		A. Shinagawa 2-16-87 MATERIAL	DATE		4-3-10, Toranomon Minato-ku, Tokyo 105
		S. Okazaki 2/18/87 APPROVED	DATE	B	SCHEMATIC DIAGRAM FOR SUB P.C.B. (MEGA ST KEYBOARD)
		J. H. Lee 3/19/87 APPROVED	DATE	C 100069	SIZE DRAWING NO.
		J. H. Lee 3/19/87 APPROVED	DATE		3
		J. H. Lee 3/19/87 APPROVED	DATE	NONE	SHEET / OF /



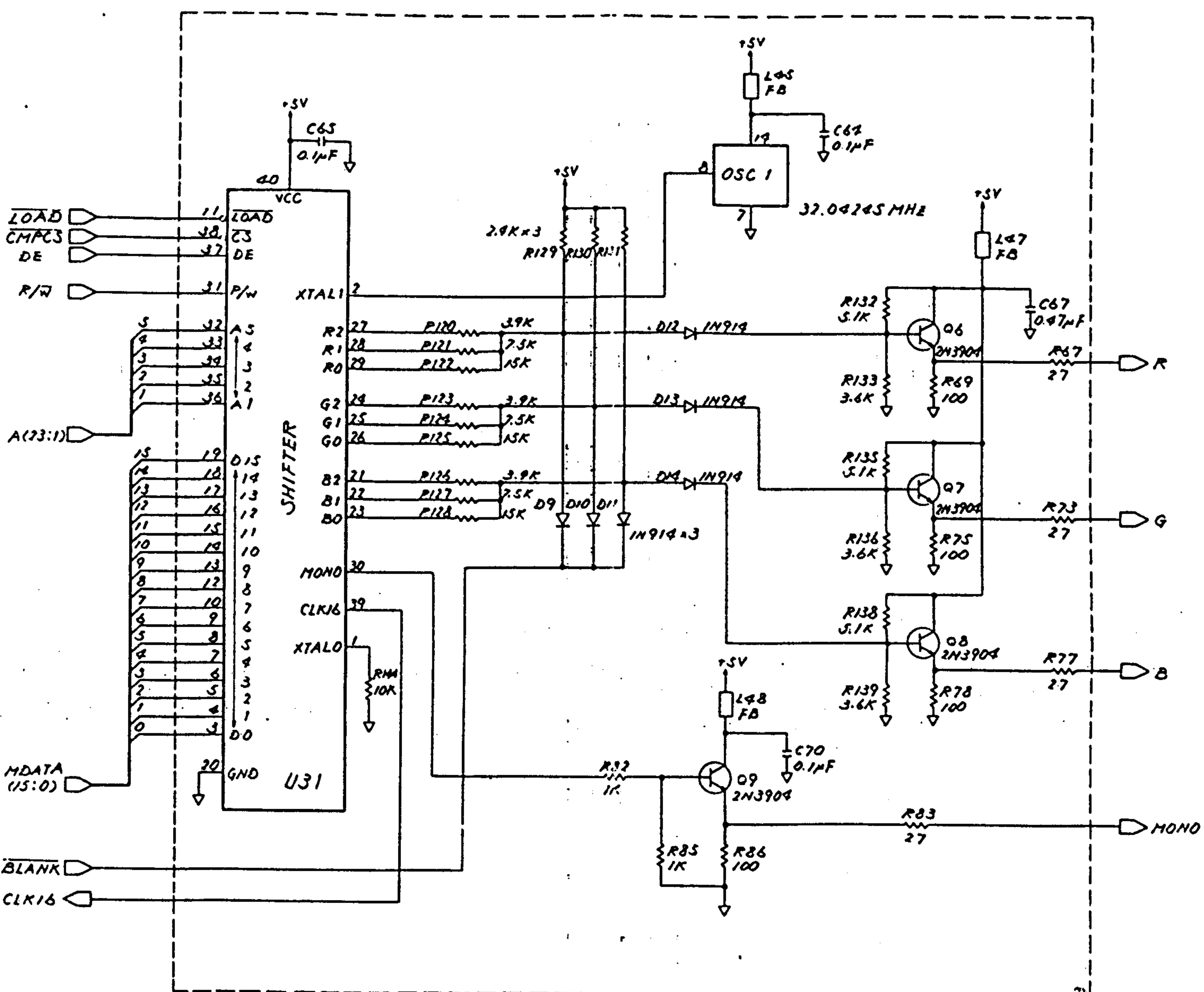




SEE SHEET 1.




PIN C070713 FULL CUSTOM W/O D TO A CONVERTER



PIN C025914 GATE ARRAY W/O D TO A CONVERTER

USED ON	DRAWN BY	DATE	ATARI (JAPAN) CORPORATION
NEXT ARRAY	H. Yamada	3-5-87	Toranomon Kiyoshi Building 3F
MATERIAL	K. Kondo	3-11-87	4-3-10, Toranomon Minato-ku, Tokyo 105
FINISH	K. Miyagi	3-27-87	SCHEMATIC DIAGRAM
	APPROVED	3-27-87	(MEGASTY 4)
	1.002461	3-17-87	SIZE: DRAWING NO.
	APPROVED	3-27-87	D C100166
	1.002461	3-17-87	REV. 9
	NONE		NOTE 4 IN 6



5 ROM TABLE

IC ; U12 (74LS11)
JUMPER ; W2, W3, W4

ROM	LOCATION			
	W2	W3	W4	U12
256K	1-2	1-2	JUMPER	NO USE
1M x 2	2-3	2-3	NON	USE
1M x 6	2-3	2-3	JUMPER	NO USE

USED ON NEXT ASSY	DRAWN BY H. Yamamoto 11-27-86	DATE 11-27-86
MATERIAL	ENGINEER K. Kita 2-16-87	DATE 3-27-87
FINISH	CHECKED K. Yamamoto	DATE
	APPROVED S. Okaishi 3/27/87	DATE
	APPROVED S. Okaishi 3/27/87	DATE

ATARI (JAPAN) CORPORATION
Toranomon Kiyoshi Building 3F
4-3-10, Toranomon Minato-ku, Tokyo 105

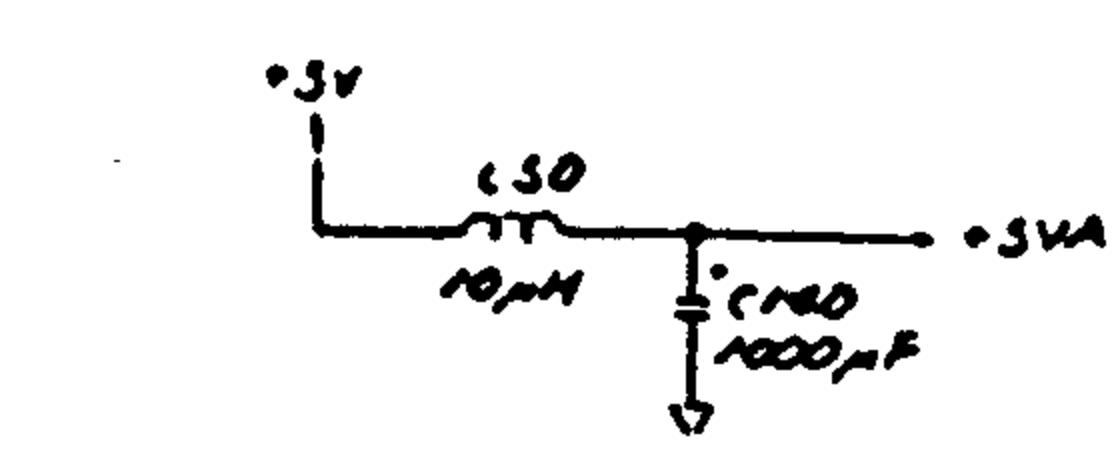
TITLE : SCHEMATIC DIAGRAM (MEGAST 8)

SIZE : DRAWING NO. C C100166


REV. 9

SCALE : NONE

SHEET 5 OF 6



SEE SHEET 1.



NO DATA
115:0

USED ON	DRAWN BY	DATE	ATARI (JAPAN) CORPORATION
NEXT A/R	H. Kawahara 11-28-86 CHECKED	DATE	Toranomon Kiyoshi Building 3F
MATERIAL	F. C. G. 7-16-87 ENGINEER	DATE	4-3-10, Toranomon Minato-ku, Tokyo 105
FINISH	K. Miyamoto APPROVED	DATE	3-27-87
	11-07-86 14717 APPROVED	DATE	11-07-86 14717
	J. H. L. 3/7/87 APPROVED	DATE	3/7/87
	M. W. K. 3/7/87 APPROVED	DATE	3/7/87
	NONE	DATE	
		REVISION	9
		NOTE	SCHEMATIC DIAGRAM (MEGAST 4)
		REVISION	D 100166